

P-224C

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Monthly Progress Report

1 July to 31 July 1960

A breadboard correlator system has been assembled following the computational scheme shown in the proposal. This includes filters, multipliers, phase-shift networks, analog integrators and amplifiers to make up phasing network losses. The final squaring and summing operations have not yet been added to the system. These functions, as well as the incoming information computation and any special bias-arrangements, will be added after sufficient observation of the existing circuitry.

At present an increasing and cumulative correlation voltage can be observed at the output of both integrators when the system is fed from the correlated test tapes. However, drift in the integrator power supplies makes it impossible to make quantitative measurements. These supplies are being replaced.

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